

FIG. 1



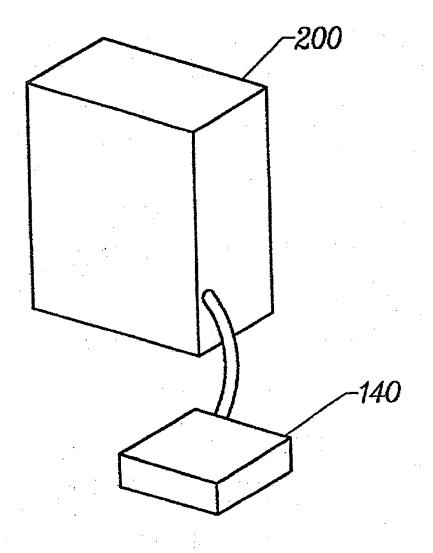


FIG. 2



PCI Bus

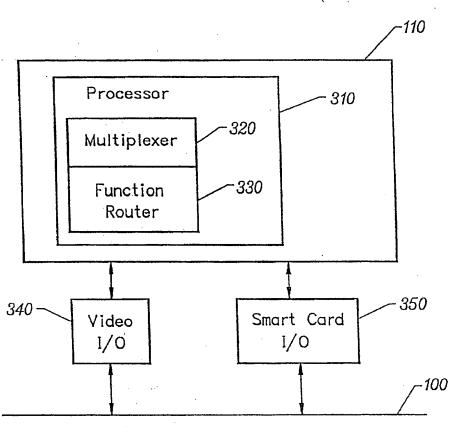


FIG. 3